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Kam-Fai Wong, Benoît Dageville

 April 1994 **Proceedings of the 1994 ACM symposium on Applied computing**

Publisher: ACM Press

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Reuben Passqini, Vernon Rego

 December 1998 **Proceedings of the 30th conference on Winter simulation**

Publisher: IEEE Computer Society Press

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3 [Adaptive two-level thread management for fast MPI execution on shared memory machines](#)



Kai Shen, Hong Tang, Tao Yang

 January 1999 **Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)**

Publisher: ACM Press

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4 [Multiple threads in cyclic register windows](#)



Yasuo Hidaka, Hanpei Koike, Hidehiko Tanaka

 May 1993 **ACM SIGARCH Computer Architecture News , Proceedings of the 20th annual international symposium on Computer architecture ISCA '93**, Volume 21 Issue 2

Publisher: ACM Press

 Full text available: pdf(1.12 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Multi-threading is often used to compile logic and functional languages, and implement parallel C libraries. Fine-grain multi-threading requires rapid context switching, which can be slow on architectures with register windows. In past, researchers have either proposed